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	Complete if Known	
Application Number	10/067,151	
Filing Date	January 29, 2002	
First Named Inventor	Rupp, Charle'	
Art Unit	2825	
Examiner Name	Do, Thuan V.	
Attomey Docket Number	PA2194US	

*		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
10		JOHN HESKETH, "The Programmable Logic Core: Enabling the configurable System-On-a-Chip", Technical Presentation by LSI Logic Corporation, presented 1/30/2001 in US	
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